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Attorney Docket No.: 12732-158001

Web Site
www.fr.com

Mail Stop Patent Application

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450



Presented for filing is a new original patent application of:

Applicant: SHIGEHARU MONOE

Title: METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE

Enclosed are the following papers, including those required to receive a filing date under 37 CFR §1.53(b):


	<u>Pages</u>
Specification	20
Claims	4
Abstract	1
Declaration	5
Drawing(s)	12

Enclosures:

- Assignment cover sheet and an assignment, 2 pages, and a separate \$40 fee.
- New disclosure information, including:
 - Information disclosure statement, 1 page.
 - PTO-1449, 1 page.
 - References, 13 items.
- Transmittal of Photographs (including Figs. 9 and 10), 3 pages.
- Transmittal of Certified Copy of Priority Document No. 2002-192384.
- Serial Number Card (green).

Under 35 USC 119, this application claims the benefit of a foreign priority application filed in Japan, serial number 2002-192384, filed July 1, 2002.

Basic filing fee	\$750
Total claims in excess of 20 times \$18	\$0
Independent claims in excess of 3 times \$84	\$84
Fee for multiple dependent claims	\$0
Total filing fee:	\$834


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A check for the filing fee is enclosed. Please apply any other required fees or any credits to deposit account 06-1050, referencing the attorney docket number shown above.

If this application is found to be incomplete, or if a telephone conference would otherwise be helpful, please call the undersigned at (202) 783-5070.

Kindly acknowledge receipt of this application by returning the enclosed postcard.

Please direct all correspondence to the following customer number:



PTO Customer No: 26171

Respectfully submitted,

A handwritten signature in dark ink, appearing to read 'John F. Hayden', written in a cursive style.

John F. Hayden

Reg. No. 37,640

Enclosures

JFH/aks

40161823.doc

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shigeharu Monoe
Serial No. : New Application
Filed : June 26, 2003
Title : METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE

Art Unit : Unknown
Examiner : Unknown

Attention: Official Draftsman
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL OF PHOTOGRAPHS

Please enter the two enclosed sheets of photographs (Figs. 9 and 10) in the application.

Applicant submits that no new matter is introduced.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: June 26, 2003



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